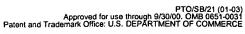


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		Filing Date	March 01, 2002
		First Named Inventor	Suresh M. Menon
		Examiner Name	Ted M. Wang
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IN THE UNITED STATES PATENT OFFICE

PATENT

Conf. No.: 4098

Applicants: Suresh M. Menon et al.

Assignee: Xilinx, Inc.

Title: "High Speed Configurable Transceiver Architecture"

Serial No.: 10/090,250 File Date: 03/01/2002

Examiner: Ted M. Wang Art Unit: 2611

Docket No.: X-1050 US Conf. No.: 4098

Mail Stop AF COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action mailed from the Patent Office on August 17, 2006, please reconsider allowance of the claims in view of the Remarks below.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 3.